

**In the Specification:**

Page 1 Under the title, please insert the following:

-- **CROSS REFERENCE TO RELATED APPLICATIONS**

This application claims the priority of PCT/GB2005/001300 filed on April 1, 2005, and, GB 0407544.6 filed on April 2, 2004, the entire contents of which are hereby incorporated in total by reference. --

Change(s)  
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to document,  
/T.C.T./  
10/28/2011

7, after line 9  
Page 8, line 4 Please add the following:

**Description of the Drawings**

An embodiment of the present invention will now be described, by way of further example only, with reference to the accompanying drawings in which: Figure 1 illustrates examples of monolithic and micro kernel architectures;

Figure 2 illustrates a procedure for waiting on a fast mutex in accordance with an embodiment of the present invention;

Figure 3 illustrates a procedure for releasing a fast mutex in accordance with an embodiment of the present invention; and

Figure 4 illustrates a procedure for optimising both waiting for and releasing a fast mutex in a single processor (non-SMP) system.